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- ## OTHER PUBLICATIONS

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- [57]
- ABSTRACT**

- An integrated data link control device (IDLC) interfaces between a host computer system and external channels in a communication network. The device contains multiple internal channels allocatable individually to interface to the external channels, each internal channel having internal buffer memory reserved to it for storing data signals handled by it. The device also includes facilities for selectively configuring groups of its internal channels into "extended channels", some of which are termed Hyper Channels. Channels in each extended channel group interface collectively and in time coordination to one external channel, presenting an effective bandwidth to the external channel which is greater than the bandwidth of any single internal channel. One channel in each group is designated as a reference channel for the group, and all data signals transferred between the group and the assigned external channel are funneled through only the buffer memory assigned to the respective reference channel; in order to ensure that such signals are transferred between the external channel and the host system in their correct "message" sequence.

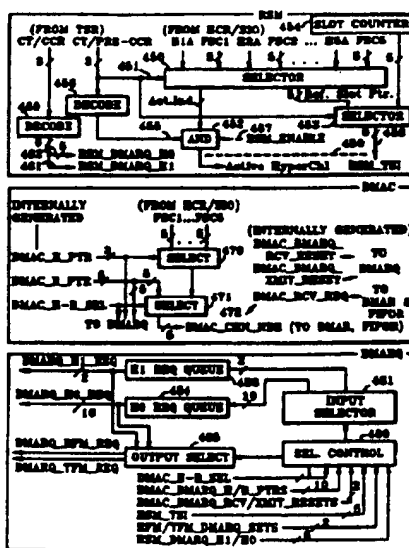
- 14 Claims, 23 Drawing Sheets**

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395/325; 395/500; 364/DIG. 2; 364/940;
364/942
[58] Field of Search 395/325, 200, 275, 700,
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Hyperchi Formulation/Usage